

*The Embedded I/O Company*



---

# TPMC861-SW-95

## QNX6 Neutrino Device Driver

4-Channel UART

Version 1.0.x

## User Manual

Issue 1.0

July 2003

---

**TEWS TECHNOLOGIES GmbH**  
Am Bahnhof 7  
Phone: +49-(0)4101-4058-0  
e-mail: info@tews.com

25469 Halstenbek / Germany  
Fax: +49-(0)4101-4058-19  
[www.tews.com](http://www.tews.com)

**TEWS TECHNOLOGIES LLC**  
1 E. Liberty Street, Sixth Floor  
Phone: +1 (775) 686 6077  
e-mail: usasales@tews.com

Reno, Nevada 89504 / USA  
Fax: +1 (775) 686 6024  
[www.tews.com](http://www.tews.com)

**TPMC861-SW-95**

4 Channel UART

QNX6 Neutrino Device Driver

This document contains information, which is proprietary to TEWS TECHNOLOGIES GmbH. Any reproduction without written permission is forbidden.

TEWS TECHNOLOGIES GmbH has made any effort to ensure that this manual is accurate and complete. However TEWS TECHNOLOGIES GmbH reserves the right to change the product described in this document at any time without notice.

TEWS TECHNOLOGIES GmbH is not liable for any damage arising out of the application or use of the device described herein.

©2003 by TEWS TECHNOLOGIES GmbH

<b>Issue</b>	<b>Description</b>	<b>Date</b>
1.0	First Issue	July 11, 2003

---

## Table of Contents

<b>1</b>	<b>INTRODUCTION.....</b>	<b>4</b>
<b>2</b>	<b>INSTALLATION.....</b>	<b>5</b>
	2.1 Build the device driver .....	5
	2.2 Start the Driver Process .....	6

---

# **1 Introduction**

The TPMC861-SW-95 QNX-Neutrino device driver is a full-duplex serial device driver which allows the operation of a TPMC861 serial PMC on Intel x86 based QNX6-Neutrino operating systems.

The TPMC861-SW-95 device driver is based on the standard QNX6 8250 serial communication manager. Due to this way of implementation the driver interface and function is compatible to the standard QNX6 serial device manager.

All standard utility programs for configuration (e.g. stty) and maintaining terminal interfaces could be used in the same manner.

Additional supported features:

- Each channel has a 128 Byte transmit and receive FIFO
- Programmable trigger level for transmit and receive FIFO

## 2 Installation

The software is delivered on a PC formatted 3½" HD diskette.

The directory A:\TPMC861-SW-95 contains the following files:

TPMC861-SW-95.pdf	This manual in PDF format
tpmc861.tar	TAR archive with driver source code

The TAR archive tpmc861.tar contains the following files and directories:

```
tpmc861/driver/common.mk
tpmc861/driver/externs.h
tpmc861/driver/externs.c
tpmc861/driver/init.c
tpmc861/driver/intr.c
tpmc861/driver/main.c
tpmc861/driver/options.c
tpmc861/driver/proto.h
tpmc861/driver/tedit.c
tpmc861/driver/tpmc861.h
tpmc861/driver/tto.c
tpmc861/driver/nto/Makefile
tpmc861/driver/nto/x86/Makefile
tpmc861/driver/nto/x86/sys_ttyinit.c
tpmc861/driver/nto/x86/o/Makefile
```

In order to perform the installation insert the diskette into the floppy drive and mount the floppy for DOS file system (e.g. `mount -t dos /dev/fd0 /floppy`). Now extract all files in the TAR archive into the directory `/usr/src` (e.g. `tar -x -f /floppy/TPMC861-SW-95/tpmc861.tar -C /usr/src`).

**Its absolute important to create the tpmc861 project directory in the /usr/src directory otherwise the automatic build with make will fail.**

**For building the device driver it is necessary that the QNX serial DDK is installed. (Installer: "/QNX Realtime Platform/Software Development/Device Driver Kits/Character (Serial) DDK targeting x86").**

### 2.1 Build the device driver

1. Change to the `/usr/src/tpmc861/driver/nto` directory
2. Execute the Makefile

```
make install
```

After successful completion the driver binary will be installed in the `/bin` directory.

## 2.2 Start the Driver Process

To start the TPMC861 device driver respective the TPMC861 serial communications manager you have to enter the process name with optional parameter from the command shell or in the startup script.

```
devc-tpmc861 [options] &
```

### OPTIONS

- b** *number* Initial baud rate (default 9600).
- C** *size* The size of the canonical buffer in bytes (default 256).
- E** Start in raw mode (the default). Software flow control is disabled by default.
- e** Start in edit mode (default raw). Software flow control is enabled by default.
- F** Disable hardware flow control (default to hardware flow control enabled).
- f** Enable hardware flow control (default). Must be disabled for TPMC861 modules.
- I** *number* The size of the interrupt input buffer in bytes (default 2048).
- O** *number* The size of the interrupt output buffer in bytes (default 2048).
- S|s** Disable / enable software flow control. The default depends on the mode: in raw mode (**-E**, the default), its disabled; in edited mode (**-e**), it's enabled. The order in which you specify the **-E** or **-e**, and **-S** or **-s** options matters:

Options	Mode	Software flow control
-e	Edited	Enabled
-S -e	Edited	Enabled
-e -S	Edited	Disabled
-E	Raw	Disabled
-s -E	Raw	Disabled
-E -s	Raw	Enabled

- r** *number* Set the receive FIFO trigger level. Every receiver trigger level from 1 to 127 is valid (default 56).
- t** *number* Set the transmit FIFO trigger level. Every transmitter trigger level from 1 to 127 is valid (default 8).
- u** *number* Append number to the device name prefix (*/dev/ser*). The default is 3, which mean the first TPMC861 device is */dev/ser3*; additional device are given increasing numbers.
- v** Print out debug information.

**Most of the options above are standard options for serial communications manager. Please refer also to related QNX6 documentation if necessary.**

## DESCRIPTION

The devc-tpmc861 manager is based on the standard QNX6 devc-ser8250 serial communications manager and can support any number of serial ports and TPMC861 PMC modules.

The devc-tpmc861 manager searches the entire PCIbus for TPMC861 devices and creates devices for each serial channel (16 per PMC). The first device created depends on the **-u** option. If the **-u** option is omitted the first TPMC861 serial device is `/dev/ser3`. In this configuration the devices `/dev/ser3`, `/dev/ser4`, ...`/dev/ser6` will be created for the first TPMC861, `/dev/ser7` ... `/dev/ser10` will be created for the second TPMC861 and so on.

Usually the device names `/dev/ser1` and `/dev/ser2` are assigned to the default PC serial ports, therefore the TPMC861 devices can start with `/dev/ser3` (default). If there are additional onboard serial devices you have to start with a higher device number for the TPMC861 devices by defining an appropriate number with the **-u** option (please check also the `/dev` directory).

**Since the TPMC861 didn't support hardware handshake (RTS/CTS) and the QNX terminal manager setup hardware handshake by default, this feature must be disabled by the **-F** option.**

A read request by default returns when at least 1 character is available. To increase efficiency, you can control three parameters to control when a read is satisfied:

*Time*            Return after a specified amount of time has elapsed (`c_cc[VTIME]`).

*Min*             Return when this number of characters is in the input buffer (`c_cc[VMIN]`).

*Char*            Return if the forwarding character is in the input buffer (`c_cc[VEND]`).

These parameters, and other, are set using library routines (see `tcgetattr()`, `txsetattr()`, `readcond()` and `TimerTimeout()` in the Library Reference).

The following fields and flags are supported in the *termios* structure.

Field	Supported fields and flags
<code>c_cc</code>	All characters
<code>c_iflag</code>	BRKINT ICRNL IGNBRK IXON
<code>c_oflag</code>	OPOST
<code>c_cflag</code>	CLOCAL CSIZE CSTOPB PARENB PARODD
<code>c_lflag</code>	ECHO ECHOE ECHOK ECHONL ICANON IEXTEN ISIG NOFLSH

## EXAMPLES

Start the device driver with default parameters (first created device is */dev/ser3*, 9600 baud, see also options above...):

```
devc-tpmc861 -F &
```

Start the device driver with default parameters and change baud rate to 38400

```
devc-tpmc861 -F -b 38400 &
```

Start the device driver with default parameters. The first created device is */dev/ser5*.

```
devc-tpmc861 -F -u 5 &
```